

seven 2 way splitters 20, cascaded to form 8-way splitter 160. 8-way splitter 160 has two 4-way splitters 140 connected by an additional splitter. Splitters 20 with substrate 40 are mounted side by side on printed circuit board 150. An input port 152 is connected to the input (port 5) of a splitter 20 which in turn is connected to two 4-way splitters 140 through circuit line 158. The outputs 153, 154, 155 and 156 are commoned through circuit line 158 to input port 5 of splitters 20. The output ports 1 and 2 of splitters 20 are connected through other circuit lines 158 to four output ports 161, 162, 163, 164, 165, 166, 167 and 168. --

In the claims:

Please delete claims 13 and 14.

Please amend the following claims:

1. (amended) A power splitter comprising:
 - a) a substrate having a plurality of layers;
 - b) a resistor formed on a top layer;
 - c) a capacitor formed between two of the layers;
 - d) a transformer attached to the top layer and electrically connected to the resistor and capacitor, the transformer providing impedance matching and dividing; and
 - e) a plurality of vias extending between the layers for providing electrical connections between the resistor, capacitor and transformer.

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5. (amended) The power splitter according to claim 4 wherein a plurality of terminals are located on the top layer.

12. (amended) A power splitter for providing impedance matching and dividing, the power splitter having an input port and a first and second output port, the power splitter comprising:

- a) a multi-layered low temperature co-fired ceramic substrate, the substrate having a top surface and a bottom surface;
- b) a plurality of terminals located on the top surface;
- c) a transformer attached to the top surface and electrically connected to the terminals;
- d) a plurality of vias extending through the substrate for providing an electrical connection between the terminals and the bottom surface;
- e) a resistor formed on the top surface and electrically connected between the first and second output ports; and
- f) a capacitor formed within the substrate and electrically connected between the transformer and a ground connection.